DWG. NO.	SH	REV
		1

REVISIONS

ZONE	REV	ECO NO.	DESCRIPTION	DATE	APPROVED
	C	3014	UPDATED TO INCLUDE REV B CHANGES, PRODUCTION RELEASE.	12-92	
	C.1	3019	ADD LEAD SPACING P/N TO BOM & SCH.	3-93	RK
	C.2	3386	UPDATE PER ECO	3-94	

16 Vrms MAX

1> Chip Select
2> Data
3> Card Address
4> Reset Address
5> Reset Address

J27 AND J28 PROGRAMMING INFORMATION

INSERT JUMPERS BETWEEN PINS 2 & 3
IF NO OTHER SIGNAL PROCESSORS PRESENT
OTHERWISE INSERT JUMPERS BETWEEN PINS 1 & 2.
(ALLOWS PREAMP MASTERING TO BE LOCATED ADJACENT
TO ONE ANOTHER IN MULTI-ZONE APPLICATIONS.)

NEXT BOARD REVISION:

2> 7805 TO 7815
7905 TO 7915
ADD 78L05 AND 79L05
OPAMPS +/- 15 VOLTS
LMC1992 +/- 5 VOLTS

NOTE:
TRIMPOTS TR1-TR8 ARE OPTIONAL

INSERT JUMPERS FOR NORMAL OPERATION.
REMOVE JUMPERS FOR INDEPENDENT OPERATION.

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LAST USED LIST			APPROVALS	DATE
			DRAWN WAYNE HARRIS	12/4/90
			CHECKED	3/9/93
			FINISH	
		DO NOT SCALE DRAWING		

TITLE
COMPUTER CONTROLLED PREAMP

SYMMETRY

SIZE	DWG NO.	REV
D	SCH-404	C.2
SCALE		SHEET
PAM		